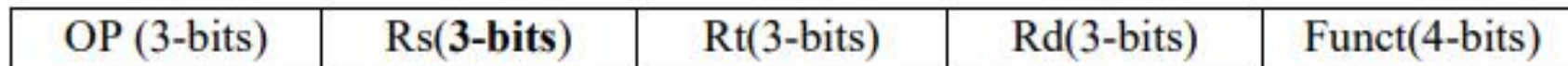


In MIPS-16 bits, the **R-type format** is given as follow:



- A. Design a code in Verilog HDL and create the symbol of the **Register File (RF)**, for **MIPS-16** processor, as explained in figure below.

